

In re application of:

Zvi OR-BACH et al.

Appl. No. 10/730,064 Confirmation No. 2943

Filed: December 9, 2003

For: METHOD FOR FABRICATION

OF SEMICONDUCTOR DEVICE

Art Unit: 2826

Examiner: Tuan N. Quach

Atty. Docket No. 38897-199163

Customer No.

26694
PATENT TRADEMARK OFFICE

Reply to Restriction Requirement

Mail Stop Non-Fee Amendment Honorable Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

In response to the Election/Restriction Requirement mailed on March 2, 2005, Applicants elect to prosecute Group I, Claims 1 and 3-12, without traverse and without prejudice to subsequently prosecute any of the non-elected claims in one or more applications to be filed at a later date.

Applicant(s): OR-BACH et al. Appl. No. 10/730,064

Should any questions arise, the Examiner is invited to contact Applicant's undersigned representative.

Respectfully submitted,

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JWG/bms DC2-631620b

Date: March 14, 2005